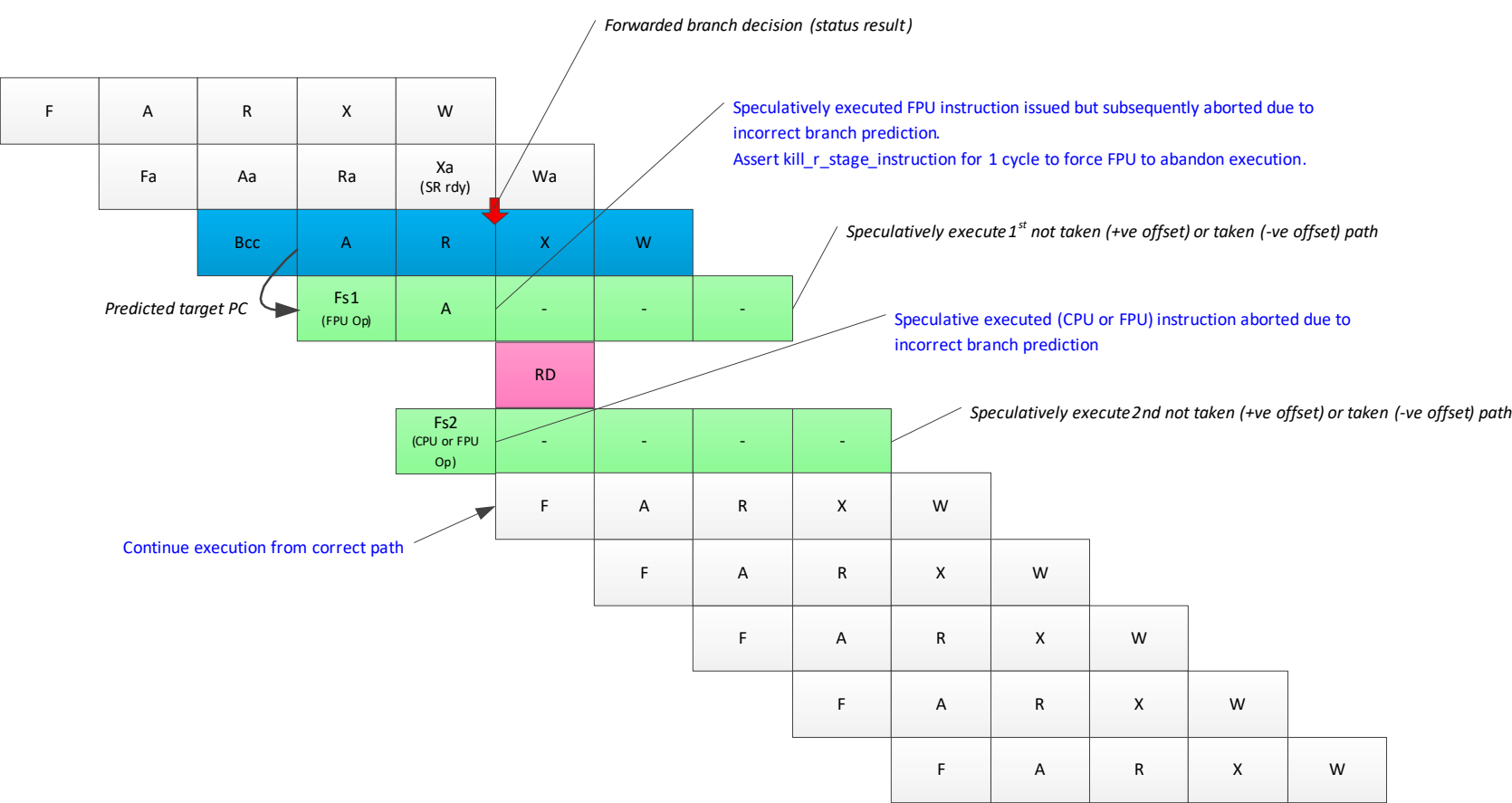
Forwarded branch decision (status result)
Speculatively executed FPU instruction issued but subsequently aborted due to incorrect branch prediction.
Assert kill_r_stage_instruction for 1 cycle to force FPU to abandon execution.
Speculatively execute 1st not taken (+ve offset) or taken (-ve offset) path
Predicted target PC
Speculative executed (CPU or FPU) instruction aborted due to incorrect branch prediction
Speculatively execute 2nd not taken (+ve offset) or taken (-ve offset) path
Continue execution from correct path
F
A
R
X
W
Fa
Aa
Ra
Xa
(SR rdy)
Wa
Bcc
Fs1
(FPU Op)
RD
Fs2
(CPU or FPU Op)
-