

CPU/SW

SW Activity

Buffer2 Read1

CHA

Other Tasks

Buffer1 Read1

CHA

Other Tasks

DMA Channel 0⁽¹⁾

CHEN (DMA0CHA[0])⁽²⁾

PCHAEN (DMA0CHA[28])⁽²⁾

Buffer 1

Buffer1 Write1

Buffer1 Write2

trg_done[1]

DMA Channel 1⁽¹⁾

CHEN (DMA1CHA[0])⁽²⁾

PCHAEN (DMA1CHA[28])⁽²⁾

Buffer 2

Buffer2 Write2

trg_done[2]

○