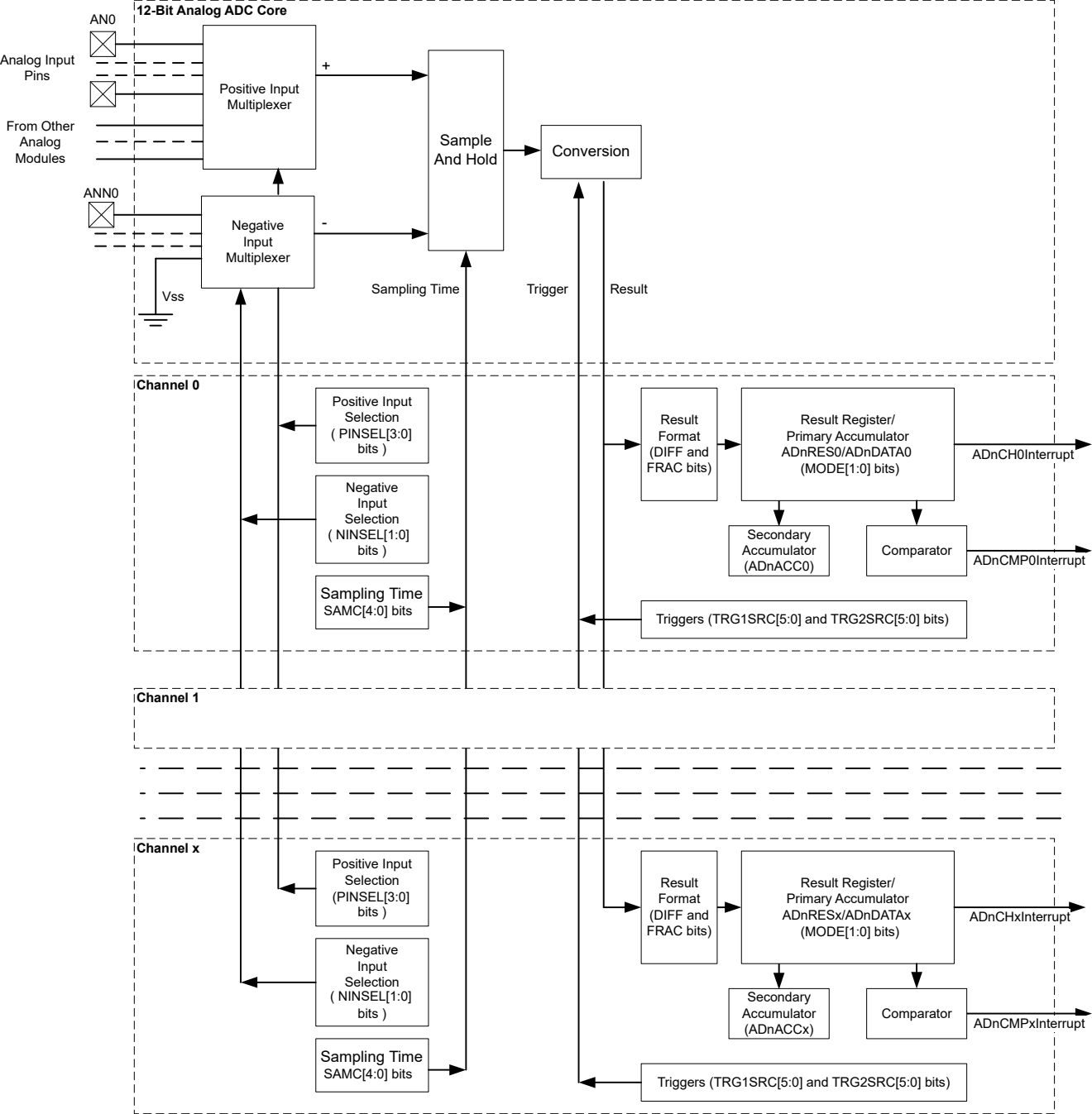
12-Bit Analog ADC Core
AN0
Analog Input Pins
From Other Analog Modules
ANN0
Positive Input Multiplexer
Negative Input Multiplexer
+
-
Sample And Hold
Conversion
Vss
Sampling Time
Trigger
Result
Channel 0
Positive Input Selection (PINSEL[3:0] bits)
Negative Input Selection (NINSEL[1:0] bits)
Sampling Time SAMC[4:0] bits
Result Format (DIFF and FRAC bits)
Result Register/ Primary Accumulator ADnRES0/ADnDATA0 (MODE[1:0] bits)
ADnCH0Interrupt
Secondary Accumulator (ADnACC0)
Comparator
ADnCMP0Interrupt
Triggers (TRG1SRC[5:0] and TRG2SRC[5:0] bits)
Channel 1
Channel x
Positive Input Selection (PINSEL[3:0] bits)
Negative Input Selection (NINSEL[1:0] bits)
Sampling Time SAMC[4:0] bits
Result Format (DIFF and FRAC bits)
Result Register/ Primary Accumulator ADnRESx/ADnDATAx (MODE[1:0] bits)
ADnCHxInterrupt
Secondary Accumulator (ADnACCx)
Comparator
ADnCMPxInterrupt
Triggers (TRG1SRC[5:0] and TRG2SRC[5:0] bits)