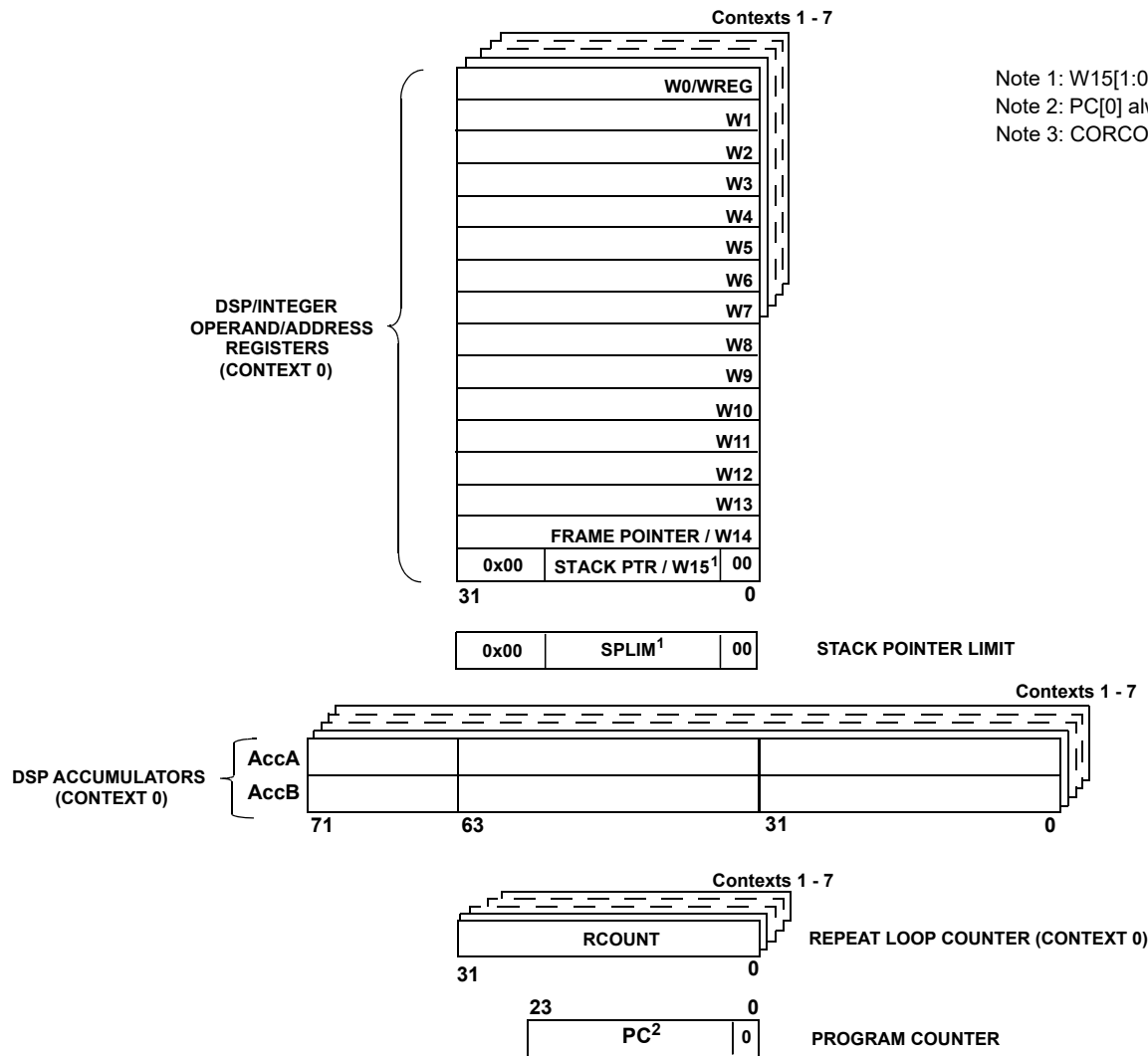




Note 1: W15[1:0] and SPLIM[1:0] always = 0b00

Note 2: PC[0] always = 0

Note 3: CORCON register is also a part of all contexts

