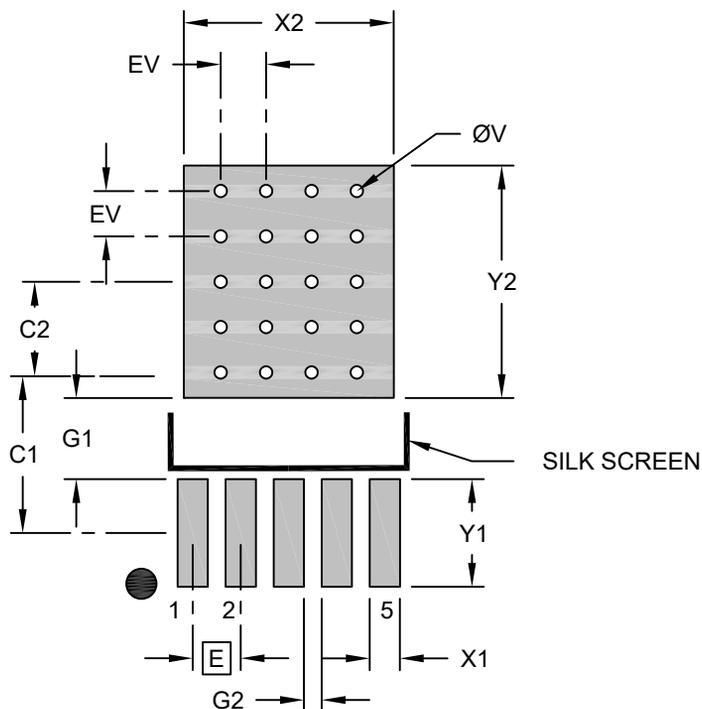


5-Lead Transistor Outline (9EA) - [TO-252]

Option "A"

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	1.27 BSC		
Center Pad Width	X2			5.55
Center Pad Length	Y2			6.15
Contact Pad Spacing	C1		4.15	
Contact Pad Spacing	C2		2.50	
Contact Pad Width (X5)	X1			0.80
Contact Pad Length (X5)	Y1			2.85
Contact Pad to Center Pad (X5)	G1	2.15		
Contact Pad to Contact Pad (X4)	G2	0.47		
Thermal Via Diameter	V		0.33	
Thermal Via Pitch	EV		1.20	

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
2. For best soldering results, thermal vias, if used, should be filled or tented to avoid solder loss during reflow process