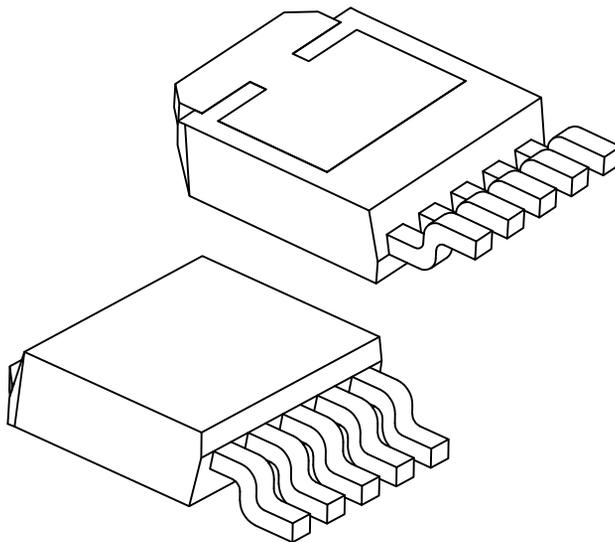


5-Lead Transistor Outline (9EA) - [TO-252]

Option "A"

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Dimension Limits		MILLIMETERS		
		Min	Nom	Max
Number of Leads	N	5		
Pitch	e	1.27 BSC		
Overall Height	A	2.18	–	2.38
Seating Plane Height	A1	–	–	0.127
Lead Width	b	0.508	–	0.771
Thermal Pad Width	b2	4.95	–	5.46
Lead Thickness	c	0.46	–	0.61
Thermal Pad Thickness	c2	0.46	–	0.89
Molded Body Length	D	5.97	–	6.22
Thermal Pad Length	D1	5.21	–	–
Total Width	E	6.35	–	6.73
Thermal Pad Width	E1	4.32	–	–
Overall Length	H	9.39	–	10.41
Foot Length	L	1.39	–	1.78
Tab Length	L1	1.067 REF		
Lead Length	L2	2.74 BSC		
Foot Angle	θ	0°	–	10°
Mold Draft Angle	θ1	0°	–	15°

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

REF: Reference Dimension, usually without tolerance, for information purposes only.