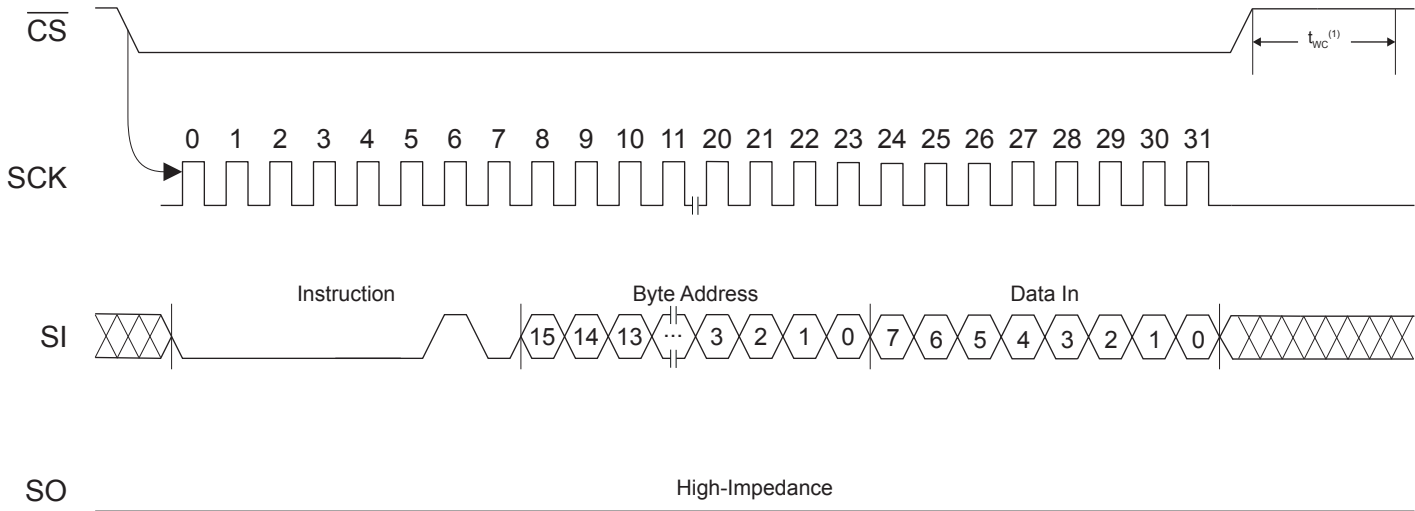




Note: This instruction initiates a self-timed internal write cycle (t_{WC}) on the rising edge of $\overline{\text{CS}}$ after a valid sequence.