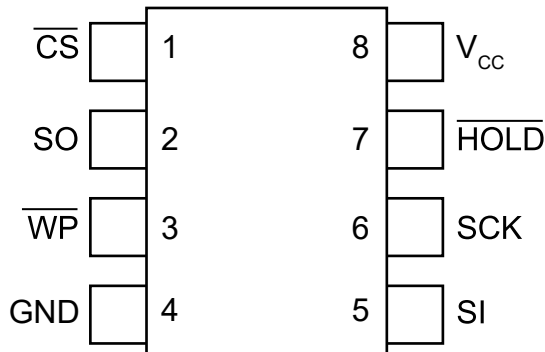
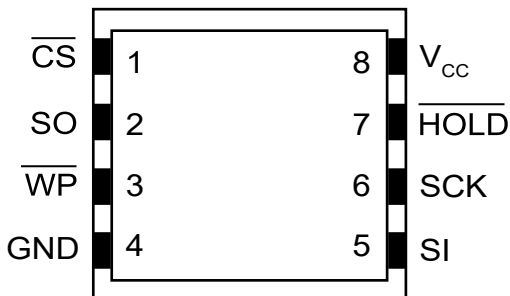


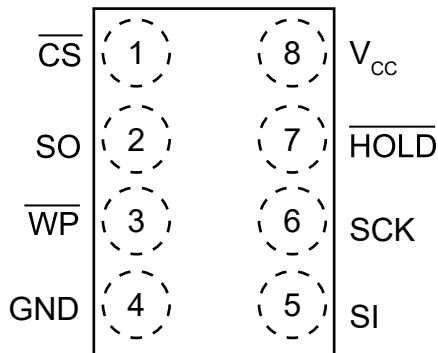
8-Lead SOIC/TSSOP (Top View)



8-Pad UDFN⁽¹⁾ (Top View)



8-Ball VFBGA (Top View)



Note 1: The exposed pad on UDFN packages can be connected to V_{SS} or left floating.