

**BANK 61**

1E80h	Core Registers
1E8Bh	
1E8Ch	ANSELA
1E8Dh	WPUA
1E8Eh	ODCONA
1E8Fh	SLRCONA
1E90h	INLVLA
1E91h	IOCAP
1E92h	IOCAN
1E93h	IOCAF
1E94h	—
1E95h	—
1E96h	ANSELB <sup>(1)</sup>
1E97h	WPUB <sup>(1)</sup>
1E98h	ODCONB <sup>(1)</sup>
1E99h	SLRCONB <sup>(1)</sup>
1E9Ah	INLVLB <sup>(1)</sup>
1E9Bh	IOCBP <sup>(1)</sup>
1E9Ch	IOCBN <sup>(1)</sup>
1E9Dh	IOCBF <sup>(1)</sup>
1E9Eh	—
1E9Fh	—
1EA0h	ANSELC
1EA1h	WPUC
1EA2h	ODCONC
1EA3h	SLRCONC
1EA4h	INLVLC
1EA5h	IOCCP
1EA6h	IOCCN
1EA7h	IOCCF
1EA8h	Unimplemented Read as '0'
1EE0h	
1EE1h	RA1I2C
1EE2h	RA2I2C
1EE3h	—
1EE4h	—
1EE5h	RB4I2C <sup>(1)</sup>
1EE6h	—
1EE7h	RB6I2C <sup>(1)</sup>
1EE8h	—
1EE9h	RC0I2C <sup>(2)</sup>
1EEAh	RC1I2C <sup>(2)</sup>
1EEBh	Unimplemented Read as '0'
1EEFh	
1EF0h	Common RAM (Accesses 70h-7Fh)
1EFFh	

**Note:**

1. Available on 20-pin devices only
2. Available on 14-pin devices only

## Legend:



Unimplemented data memory locations, read as '0'