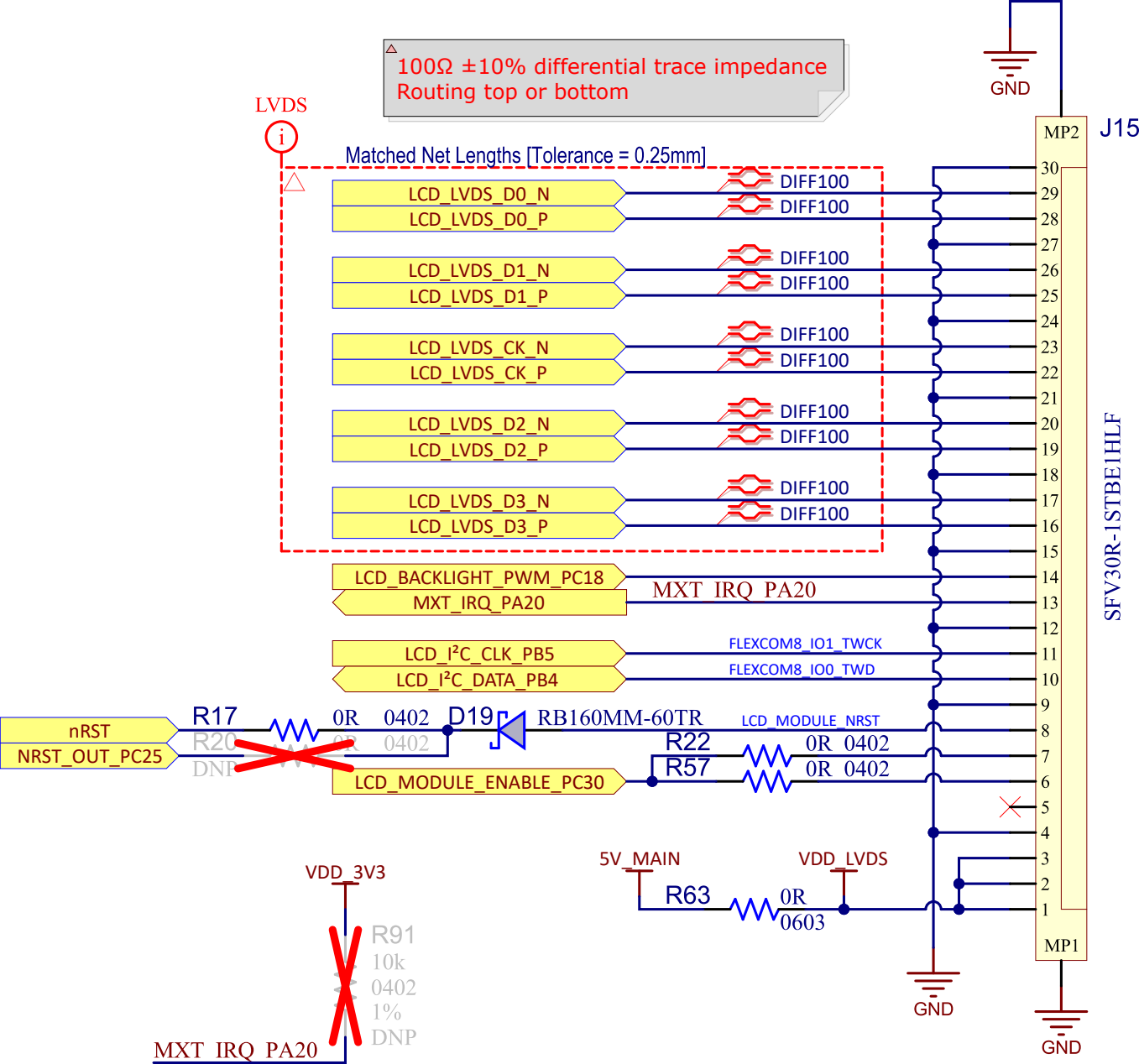
100Ω ±10% differential trace impedance
Routing top or bottom
LVDS
Matched Net Lengths [Tolerance = 0.25mm]
LCD_LVDS_D0_N
LCD_LVDS_D0_P
LCD_LVDS_D1_N
LCD_LVDS_D1_P
LCD_LVDS_CK_N
LCD_LVDS_CK_P
LCD_LVDS_D2_N
LCD_LVDS_D2_P
LCD_LVDS_D3_N
LCD_LVDS_D3_P
DIFF100
LCD_BACKLIGHT_PWM_PC18
MXT_IRQ_PA20
LCD_I²C_CLK_PB5
LCD_I²C_DATA_PB4
FLEXCOM8_IO1_TWCK
FLEXCOM8_IO0_TWD
nRST
NRST_OUT_PC25
R17
0R 0402
R20
0R 0402
DNP
D19
RB160MM-60TR
LCD_MODULE_NRST
R22
0R 0402
R57
0R 0402
LCD_MODULE_ENABLE_PC30
5V_MAIN
VDD_LVDS
R63
0R
0603
VDD_3V3
R91
10k
0402
1%
DNP
MXT_IRQ_PA20
GND
MP2
J15
MP1
SFV30R-1STBE1HLF