





























PA7		1	28		PA6
PC0		2	27		PA5
PC1		3	26		PA4
PC2		4	25		PA3
PC3		5	24		PA2
PD0		6	23		PA1 (XTALHF2)
PD1		7	22		PA0 (XTALHF1)
PD2		8	21		GND
PD3		9	20		VDD
PD4		10	19		PF7 (UPDI)
PD5		11	18		PF6 ($\overline{\text{RESET}}$)
PD6		12	17		PF1 (XTAL32K2)
PD7		13	16		PF0 (XTAL32K1)
VDD		14	15		GND