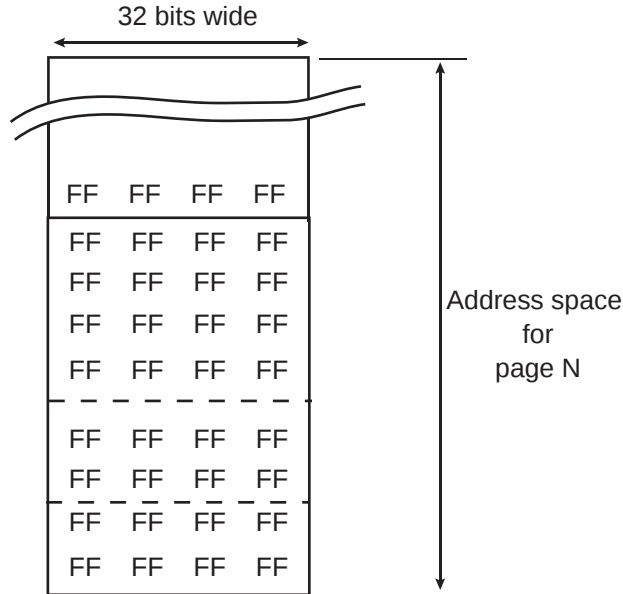
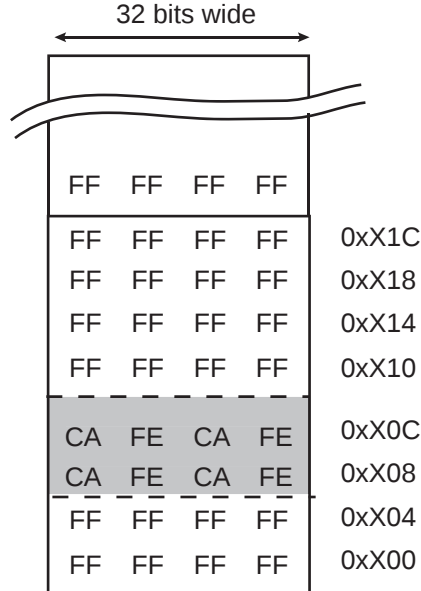
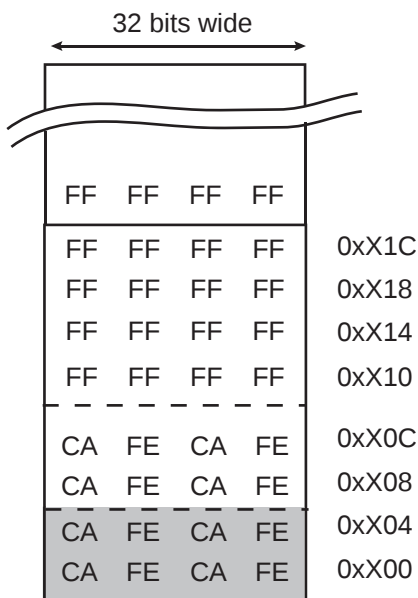




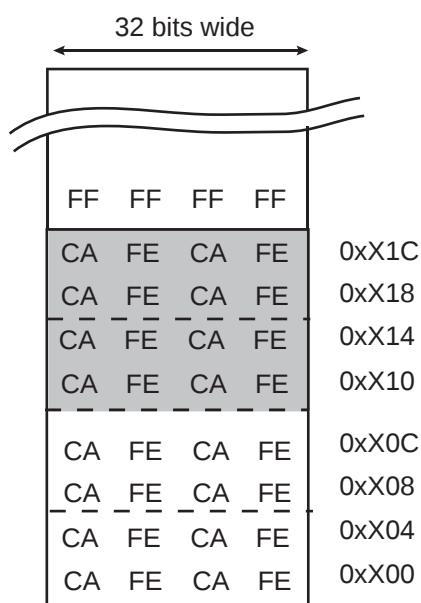
Step 1: Flash array after pages erase



Step 2: Flash array after programming
64-bit at address 0xX08 (write latch buffer + WP)



Step 3: Flash array after programming
a second 64-bit data at address 0xX00
(write latch buffer + WP)



Step 4: Flash array after programming
a 128-bit data word at address 0xX10
(write latch buffer + WP)