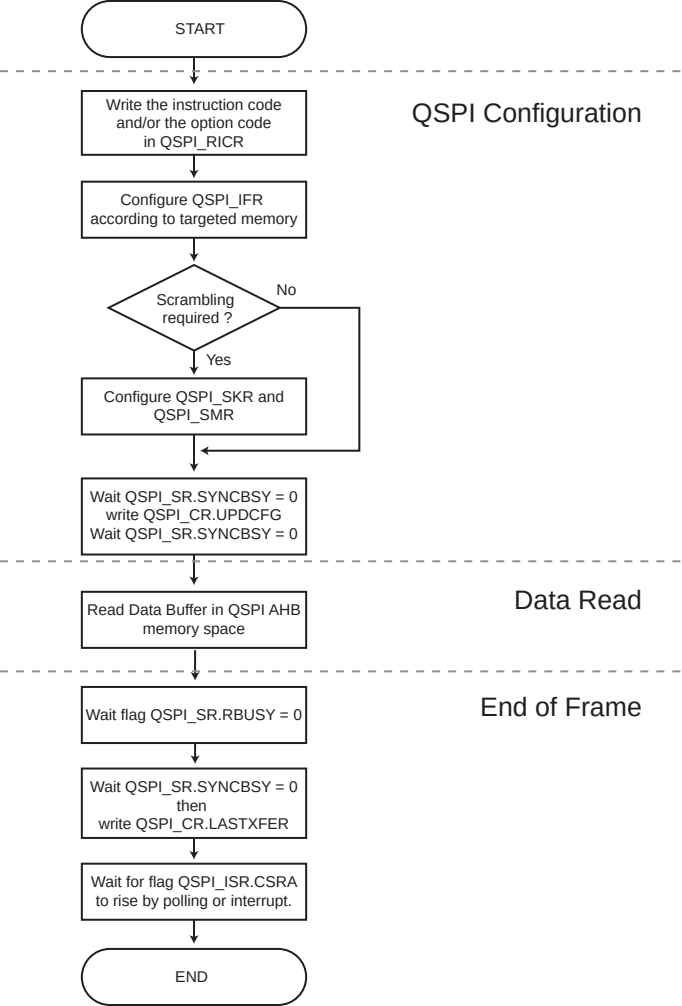
START
QSPI Configuration
Write the instruction code and/or the option code in QSPI_RICR
Configure QSPI_IFR according to targeted memory
Scrambling required ?
No
Yes
Configure QSPI_SKR and QSPI_SMR
Wait QSPI_SR.SYNCBSY = 0
write QSPI_CR.UPDCFG
Wait QSPI_SR.SYNCBSY = 0
Data Read
Read Data Buffer in QSPI AHB memory space
End of Frame
Wait flag QSPI_SR.RBUSY = 0
Wait QSPI_SR.SYNCBSY = 0 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt.
END