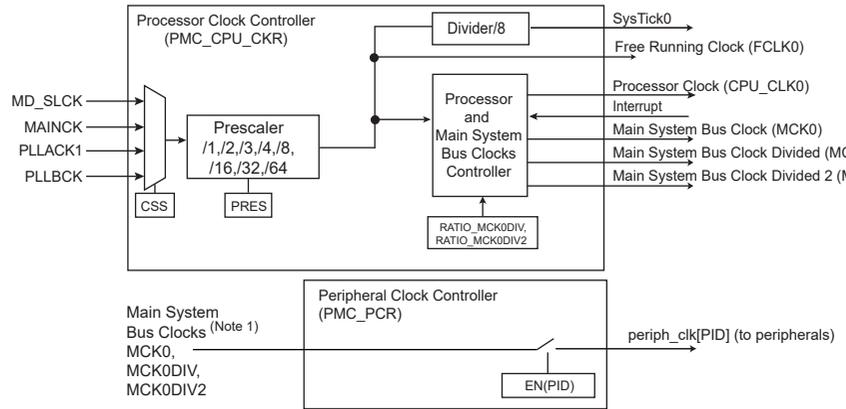
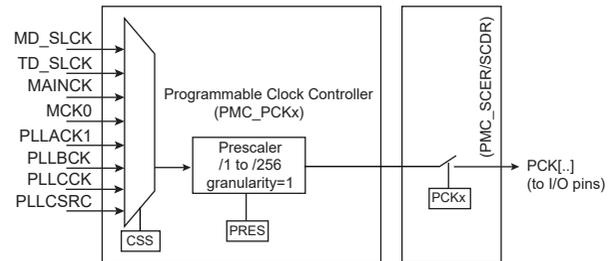
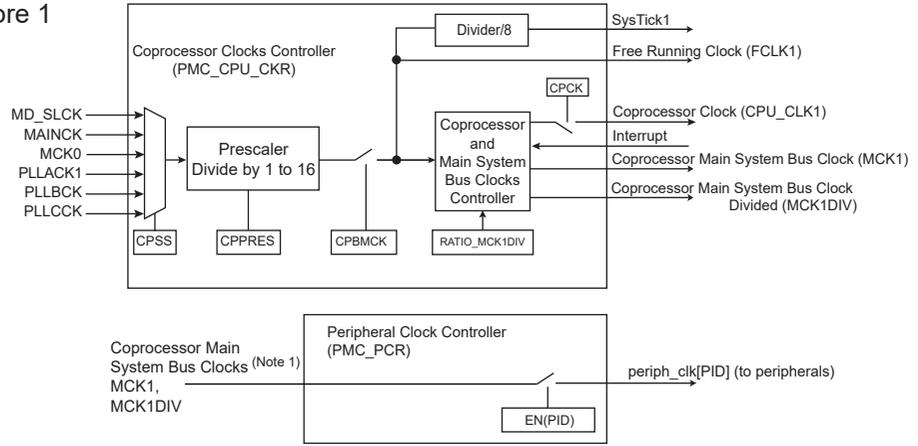


Core 0



Core 1



Note: (1) See table "Peripheral Identifiers" for the Main System Bus Clocks used versus peripherals.