

## BANK 60

|       |                              |       |                                     |
|-------|------------------------------|-------|-------------------------------------|
| 1E00h | Core Registers               | 1E3Ah | Unimplemented<br>Read as '0'        |
| 1E0Bh |                              | 1E3Ch |                                     |
| 1E0Ch | PPSLOCK                      | 1E3Dh | CLCIN0PPS                           |
| 1E0Dh | INTPPS                       | 1E3Eh | CLCIN1PPS                           |
| 1E0Eh | TOCKIPPS                     | 1E3Fh | CLCIN2PPS                           |
| 1E0Fh | T1CKIPPS                     | 1E40h | CLCIN3PPS                           |
| 1E10h | T1GPPS                       | 1E41h | DT1PPS                              |
| 1E11h | T3CKIPPS                     | 1E42h | RX1PPS                              |
| 1E12h | T3GPPS                       | 1E43h | DT2PPS                              |
| 1E13h | Unimplemented<br>Read as '0' | 1E44h | RX2PPS                              |
| 1E18h |                              | 1E45h | —                                   |
| 1E19h | T2INPPS                      | 1E46h | —                                   |
| 1E1Ah | T4INPPS                      | 1E47h | SSP1CLKPPS                          |
| 1E1Bh | Unimplemented<br>Read as '0' | 1E48h | SSP1DATPPS                          |
| 1E1Dh |                              | 1E49h | SSP1SSPPS                           |
| 1E1Eh | CCP1PPS                      | 1E4Ah | SSP2CLKPPS                          |
| 1E1Fh | CCP2PPS                      | 1E4Bh | SSP2DATPPS                          |
| 1E20h | Unimplemented<br>Read as '0' | 1E4Ch | SSP2SSPPS                           |
| 1E23h |                              | 1E4Dh | Unimplemented<br>Read as '0'        |
| 1E24h | PWMIN0PPS                    | 1E4Fh |                                     |
| 1E25h | PWMIN1PPS                    | 1E50h | ADACTPPS                            |
| 1E26h | PWM1ERSPPS                   | 1E51h | Unimplemented<br>Read as '0'        |
| 1E27h | PWM2ERSPPS                   | 1E6Fh |                                     |
| 1E28h | Unimplemented<br>Read as '0' | 1E70h | Common RAM<br>(Accesses<br>70h-7Fh) |
| 1E38h |                              | 1E7Fh |                                     |
| 1E39h | CWG1PPS                      |       |                                     |

Legend:



Unimplemented data memory locations, read as '0'.