

## BANK 61

|       |                        |       |                                     |
|-------|------------------------|-------|-------------------------------------|
| 1E80h | Core Registers         | 1EA0h | ANSELC                              |
| 1E8Bh |                        | 1EA1h | WPUC                                |
| 1E8Ch |                        | 1EA2h | ODCONC                              |
| 1E8Dh |                        | 1EA3h | SLRCONC                             |
| 1E8Eh |                        | 1EA4h | INLVLC                              |
| 1E8Fh | ANSELA                 | 1EA5h | IOCCP                               |
| 1E90h | WPUA                   | 1EA6h | IOCCN                               |
| 1E91h | ODCONA                 | 1EA7h | IOCCF                               |
| 1E92h | SLRCONA                | 1EA8h | Unimplemented<br>Read as '0'        |
| 1E93h | INLVLA                 | 1EE4h |                                     |
| 1E94h | IOCAP                  | 1EE5h | RB4I2C <sup>(2)</sup>               |
| 1E95h | IOCAN                  | 1EE6h | RB5I2C <sup>(2)</sup>               |
| 1E96h | IOCAF                  | 1EE7h | RB6I2C <sup>(2)</sup>               |
| 1E97h | —                      | 1EE8h | RB7I2C <sup>(2)</sup>               |
| 1E98h | —                      | 1EE9h | RC0I2C <sup>(3)</sup>               |
| 1E99h | ANSELB <sup>(1)</sup>  | 1EEAh | RC1I2C <sup>(3)</sup>               |
| 1E9Ah | WPUB <sup>(1)</sup>    | 1EEBh | —                                   |
| 1E9Bh | ODCONB <sup>(1)</sup>  | 1EECh | RC4I2C <sup>(3)</sup>               |
| 1E9Ch | SLRCONB <sup>(1)</sup> | 1EEDh | RC5I2C <sup>(3)</sup>               |
| 1E9Dh | INLVLB <sup>(1)</sup>  | 1EEEh | —                                   |
| 1E9Eh | IOCBP <sup>(1)</sup>   | 1EEFh | —                                   |
| 1E9Fh | IOCBN <sup>(1)</sup>   | 1EF0h | Common RAM<br>(Accesses<br>70h-7Fh) |
|       | IOCBF <sup>(1)</sup>   | 1EFFh |                                     |
|       | —                      |       |                                     |
|       | —                      |       |                                     |

### Notes:

1. Available on 20-pin devices only.
2. Available on PIC16F18146 only.
3. Available on PIC16F18126 only.

### Legend:



Unimplemented data memory locations, read as '0'.