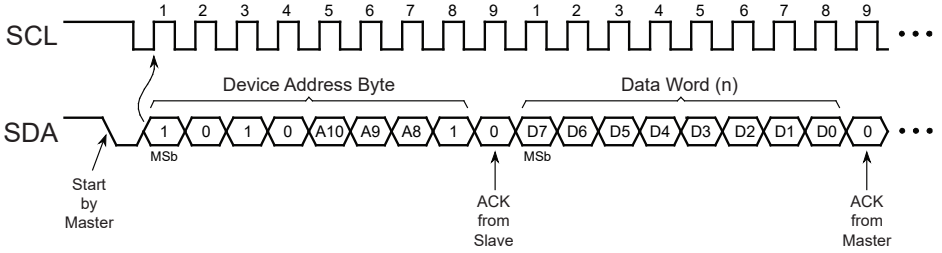
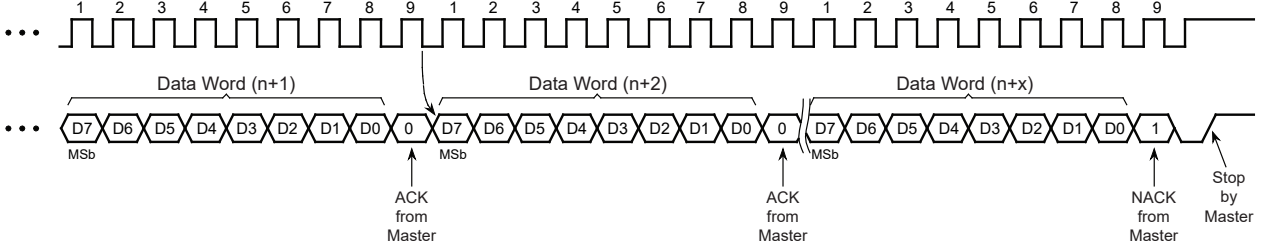

SCL
SDA
1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9
Device Address Byte
Data Word (n)
1 0 1 0 A10 A9 A8 1 0 D7 D6 D5 D4 D3 D2 D1 D0 0
MSb
MSb
Start by Master
ACK from Slave
ACK from Master


1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9
Data Word (n+1)
Data Word (n+2)
Data Word (n+x)
D7 D6 D5 D4 D3 D2 D1 D0 0 D7 D6 D5 D4 D3 D2 D1 D0 0 D7 D6 D5 D4 D3 D2 D1 D0 1
MSb
MSb
MSb
ACK from Master
ACK from Master
NACK from Master
Stop by Master