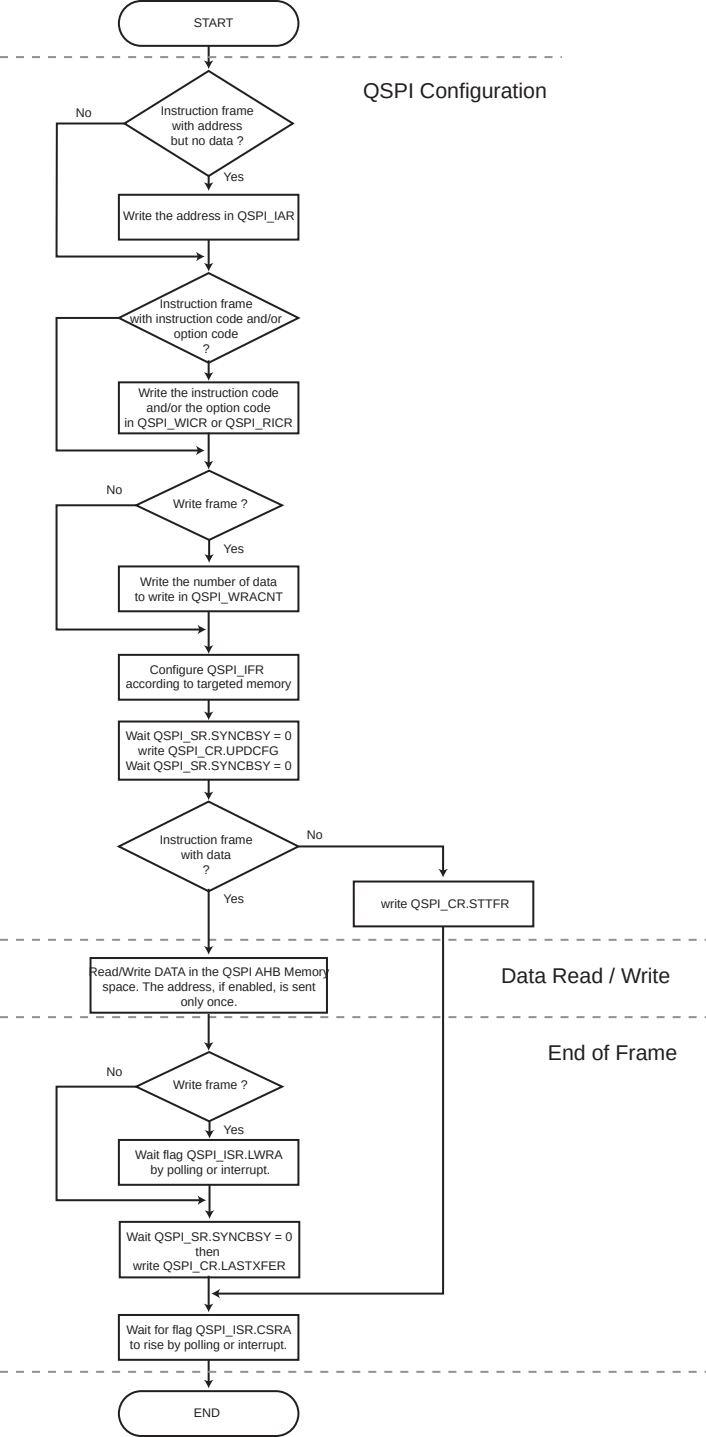
START
QSPI Configuration
Instruction frame with address but no data ?
No
Yes
Write the address in QSPI_IAR
Instruction frame with instruction code and/or option code ?
Write the instruction code and/or the option code in QSPI_WICR or QSPI_RICR
Write frame ?
No
Yes
Write the number of data to write in QSPI_WRACNT
Configure QSPI_IFR according to targeted memory
Wait QSPI_SR.SYNCBSY = 0
write QSPI_CR.UPDCFG
Wait QSPI_SR.SYNCBSY = 0
Instruction frame with data ?
No
Yes
write QSPI_CR.STTFR
Data Read / Write
Read/Write DATA in the QSPI AHB Memory space. The address, if enabled, is sent only once.
End of Frame
Write frame ?
No
Yes
Wait flag QSPI_ISR.LWRA by polling or interrupt.
Wait QSPI_SR.SYNCBSY = 0 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt.
END